REMARKS

Claims 1-16, 41-50, and 66-85 are pending in the current application. Claims 1-16, 41-50, 68-79 and 82-85 are rejected.   Claims 6-8, 66-68, 71-75, and 78-79 are amended have been amended herein. **If the Examiner does not allow all of the claims after considering this Response, the undersigned requests that the Examiner contact him to schedule and conduct a telephone interview before issuing a subsequent Office Action.**

Claim Objection

Claims 6-8, 66-68, 71-75, and 78-79 were objected to because of informalities. Claims 6-8, 66-68, 71-75, and 78-79 are amended. Applicants' Attorney therefore requests withdrawal of this objection.

Rejection Of Claims 1-16, 41-50, and 65-85 Under 35 U.S.C. § 112 As failing to comply with the written description requirement

Claim 1, for example, recites generating an identifier indicating at least one processing pipeline without using a virtual address.

Examiner asserts that the element "without using a virtual address" is new matter with respect to the original specification and that the specification is silent as to whether addresses are physical or virtual.

However, referring to FIG. 5 and paragraph [84] of the specification, a message header may include a network address or bus-port address (*i.e.*, a physical address). Referring, *e.g.*, to FIGS. 4-5 and paragraphs [94] – [102] of the patent application, in an embodiment, a hardwired-pipeline circuit 80 is operable to process data with an indicated processing pipeline 74 without receiving a message indicating the processing pipeline using a virtual address. (see, *e.g.*, paragraph [98]) An input-data handler 120 dissects a message that may have a data payload (*i.e.*, data to be processed by a pipeline 74); a pipeline unit address (*e.g.*, a network address or bus-port

address for pipeline unit 78, which comprises pipelines $74_1 - 74_n$); and a pipeline identifier (*e.g.*, an identifier of one of pipelines $74_1 - 74_n$).   The data payload is written to a location in DPSRAM 100. A pointer to this location of the data payload and an indication of a destination pipeline (*i.e.*, one of pipelines $74_1 - 74_n$) is stored in an input data queue 112. This pipeline indicator relates to a physical address of a pipeline and not a virtual address. When the selected pipeline is ready to process the data payload, the pointer to the data payload location is sent to the selected pipeline, and the selected pipeline retrieves and processes the data payload.

Rejection Of Claims 1-16, 41-50, and 65-85 Under 35 U.S.C. § 112 As failing to comply with enablement requirement

Claim 1, for example, recites receiving a message that includes data and that includes a header having information indicating at least one but fewer than all of a plurality of processing pipelines and generating an identifier indicating at least one processing pipeline without using a virtual address.

Examiner asserts that header information is not a physical address and that generating an identifier requires use of a virtual address.

However, referring to FIG. 5 and paragraph [84] of the specification, a message header may include a network address or bus-port address (*i.e.*, a physical address). Referring, *e.g.*, to FIGS. 4-5 and paragraphs [94] – [102] of the patent application, in an embodiment, a hardwired-pipeline circuit 80 is operable to process data with an indicated processing pipeline 74 without receiving a message indicating the processing pipeline using a virtual address. (see, *e.g.*, paragraph [98]) An input-data handler 120 dissects a message that may have a data payload (*i.e.*, data to be processed by a pipeline 74); a pipeline unit address (*e.g.*, a network address or bus-port address for pipeline unit 78, which comprises pipelines $74_1 - 74_n$); and a pipeline identifier (*e.g.*, an identifier of one of pipelines $74_1 - 74_n$).   The data payload is written to a location in DPSRAM 100. A pointer to this location of the data payload and an indication of a destination pipeline (*i.e.*, one of pipelines $74_1 - 74_n$) is stored in an input data queue 112. This pipeline indicator relates to a physical address of a pipeline and

not a virtual address. When the selected pipeline is ready to process the data payload, the pointer to the data payload location is sent to the selected pipeline, and the selected pipeline retrieves and processes the data payload.

CONCLUSION

In the event additional fees are due as a result of this amendment, the Commissioner is hereby authorized to charge any deficiency of fees submitted herewith, or credit any overpayment, to Deposit Account No. 07-1897.

If the Examiner believes that a telephone interview would be helpful, he is respectfully requested to contact the Applicant's attorney at (425) 455-5575.

Dated this 3rd day of May, 2011.

Respectfully submitted,

  /Dylan O. Adams/

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